



## Dr. Narayan A. Badiger

Associate Professor

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**Date of Joining** 01 Feb 2014

### Educational Qualifications

#	Degree	Specialization	University	Year	PhD Guide
1		Electronics & Instrumentation	VTU Belagavi	2006	
2		VLSI Design & Embedded SYstem	VTU Belagavi	2013	
3		Low Power VLSI using Fin-FET Technology	VTU Belagavi	2024	

### Experience

#	Type	Duration	Organization	Description
1	Teaching	1y 0m	VTU Belagavi	
2	Teaching	13y 0m	SGBIT Belagavi	

### Professional Memberships

ISTE (103816) since 2015

International Association of Engineers (IAENG) (240392) since 2019

### Achievements & Awards

**1. Article titled "Design of efficient Phase Frequency detector for Low Power PLL using GPK 45nm technology" at IEEE International Conference on Network, Multimedia and Information Technology (N — Best Paper (1))**

IEEE

**2. Power and Delay Analysis of a CMOS Inverter" at IEEE International Conference on Data Science and Network Security (ICDSNS-2023) — Best Paper**

IEEE

